AMENDMENTS TO THE SPECIFICATION

Please replace Paragraph [0052] with the following paragraph rewritten in amendment format:

[0052] According to exemplary embodiments, phase controller 110 alters the phase of each information communication clock signal of each of the plurality of information communication devices 105 by a predetermined amount. In other words, phase controller 110 can stagger the phase of each information communication clock signal of each of the plurality of information communication devices 105 relative to each other. The predetermined amount can be any suitable change in phase in each of the information communication clock signals relative to the other information communication clock signals. According to one exemplary embodiment of the present invention, phase controller 110 alters the phase of each information communication clock signal of each of the plurality of information communication devices 105 by a multiple of 90 degrees. For example, if there are four information communication devices 105, then the corresponding four information communication clock signals can each be staggered in phase by 90 degrees relative to each other by phase controller [[100]]110 according to, for example, (0°, 90°, 180°, 270°), (0°, 180°, 270°, 90°), or any other suitable combination of phases such that the phases of the individual information communication clock signals are not aligned. For example, if there are eight information communication devices 105, then the corresponding eight communication clock signals can each be staggered in phase by 45 degrees relative to each other by phase controller 110 according to, for example, (0°, 45°, 90°, 135°, 180°, 225°, 270°, 315°) or any other suitable combination of phases.

Please replace Paragraph [0058] with the following paragraph rewritten in amendment format:

[0058] FIG. 3 is a block diagram illustrating a system for controlling phase of clock signals among a plurality of information communication devices 105 of an information communication system 100 using a phase controller 110 comprised of a plurality of time-delayed communication channels, in accordance with an alternative exemplary embodiment of the present invention. According to the alternative exemplary embodiment, the phase controller 110 includes at least one delay locked loop 305. The at least one delay locked loop 305 is in communication with each of the plurality of information communication devices 105 via an information communication channel 312. For example, the delay locked loop 305 can be in communication with each of the plurality of information communication devices 105 using a separate information communication channel 312, or an information communication channel 312 can be in communication with groups of information communication devices 105. To control the amount of time delay of the common reference clock signal 122 into each of the plurality of information communication devices 105, each information communication channel 312 includes at least one of the plurality of time delay elements [[205]]310. The amount of time delay introduced through each information communication channel 312 is altered by, for example, changing the number of time delay elements [[205]]310 located along an information communication channel 312, changing the amount of time delay supplied by the one or more time delay elements [[205]]310 in the information communication channel 312, and the like. The embodiment illustrated in FIG. 3 can also be used to address issues related to clock skew.